

図 1

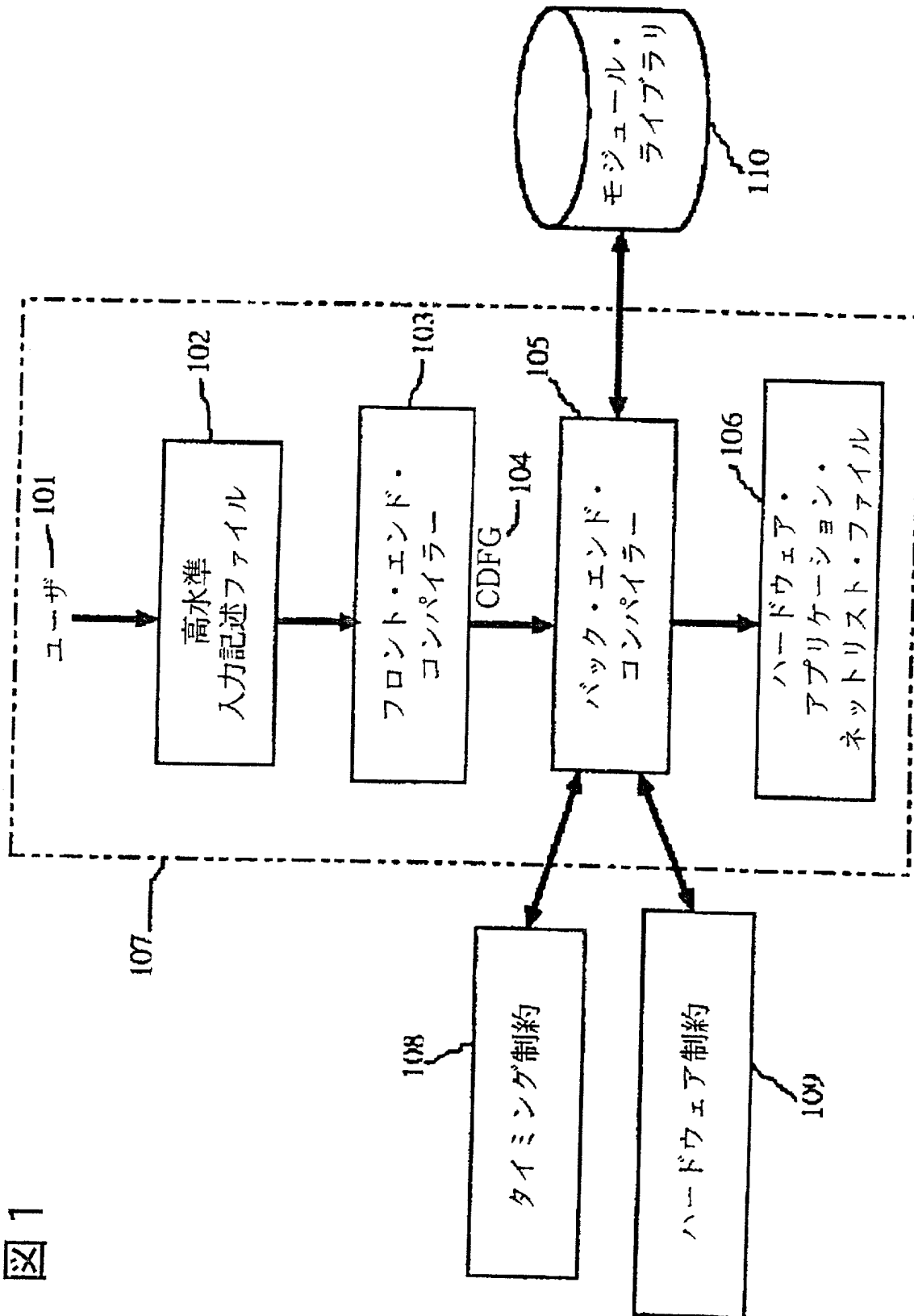


FIG. 2a

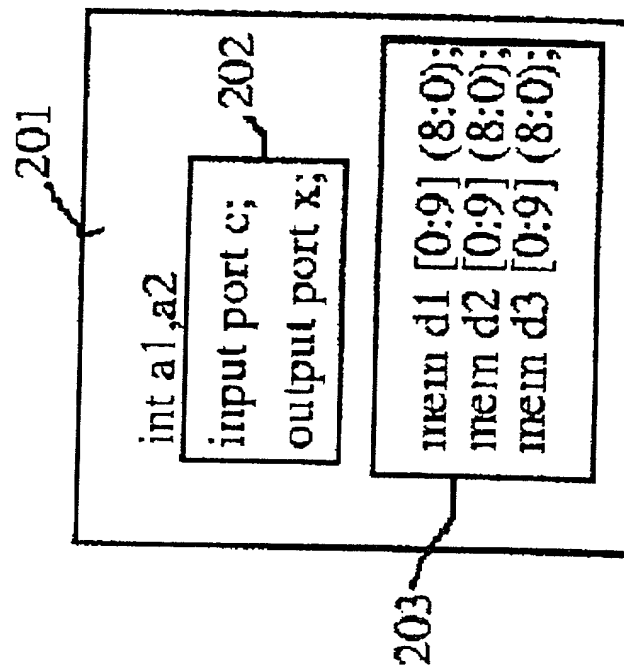


FIG. 2b

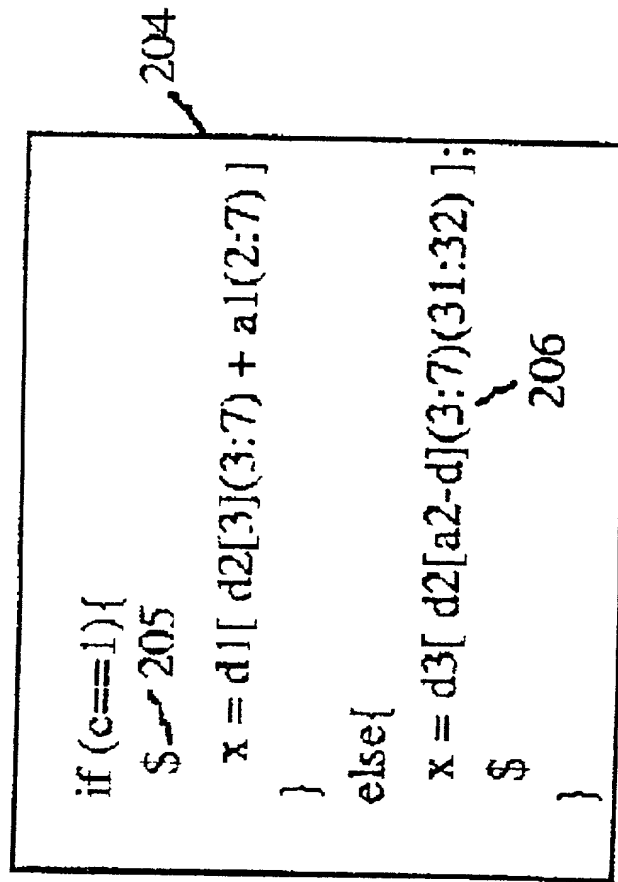
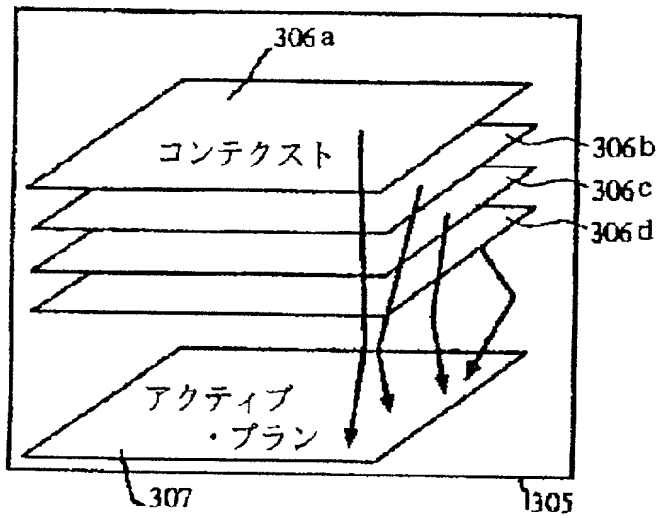
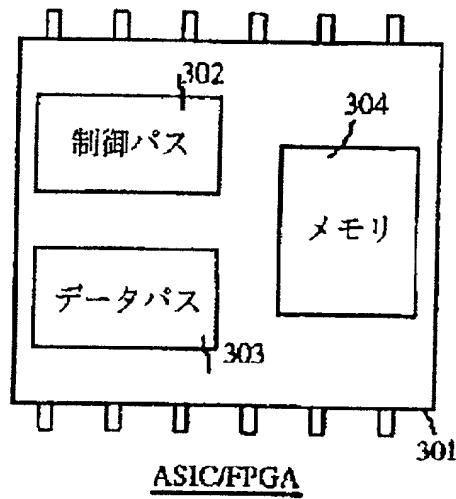
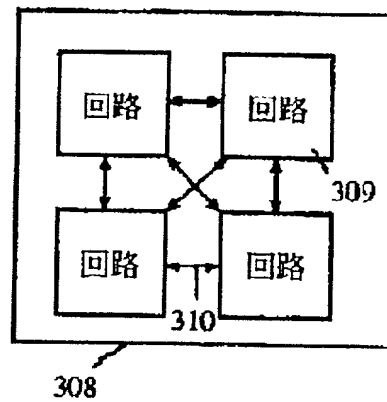


図 3



Dynamic Reconfigurable Hardware (DRI)



Multi-chip circuit

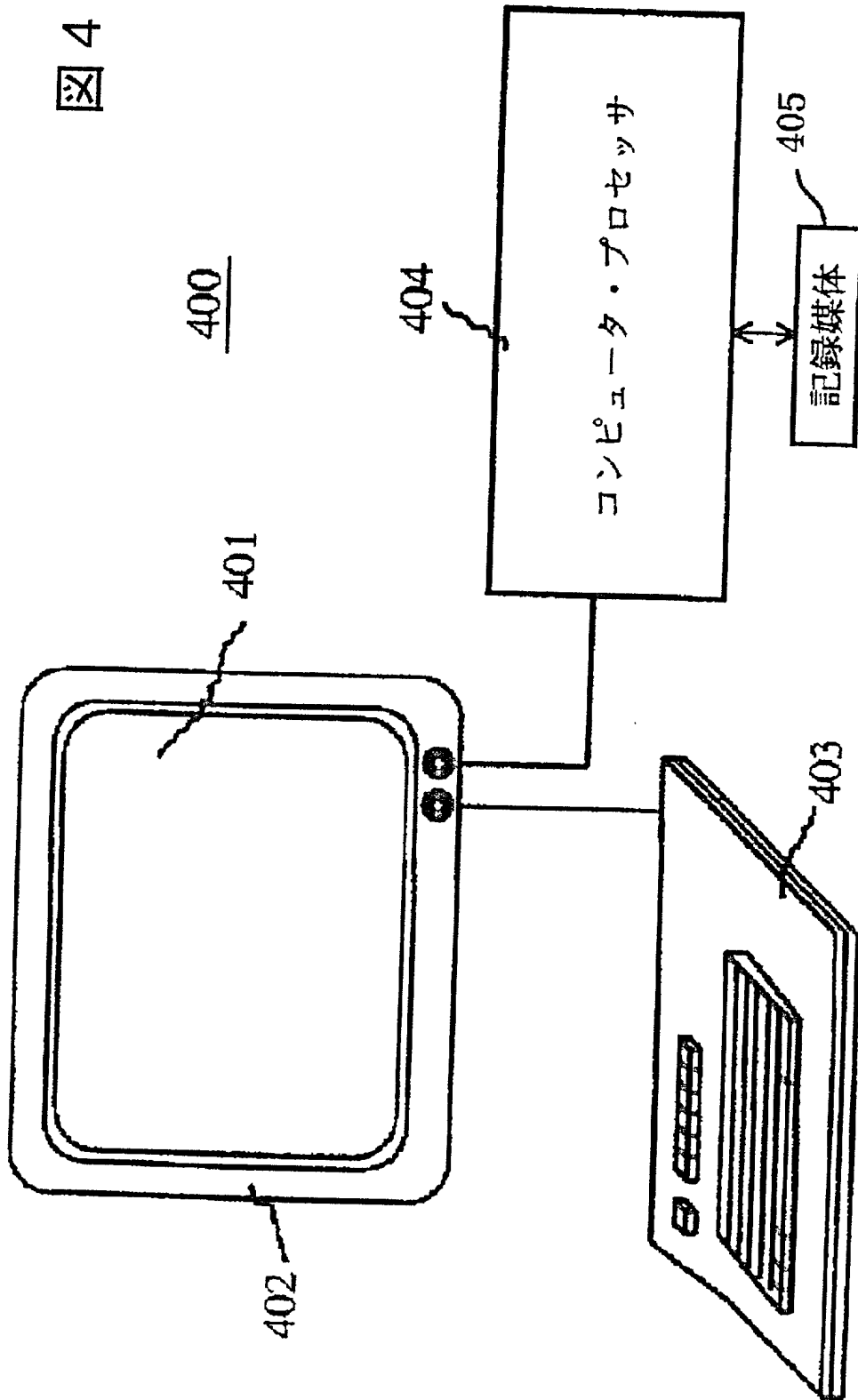
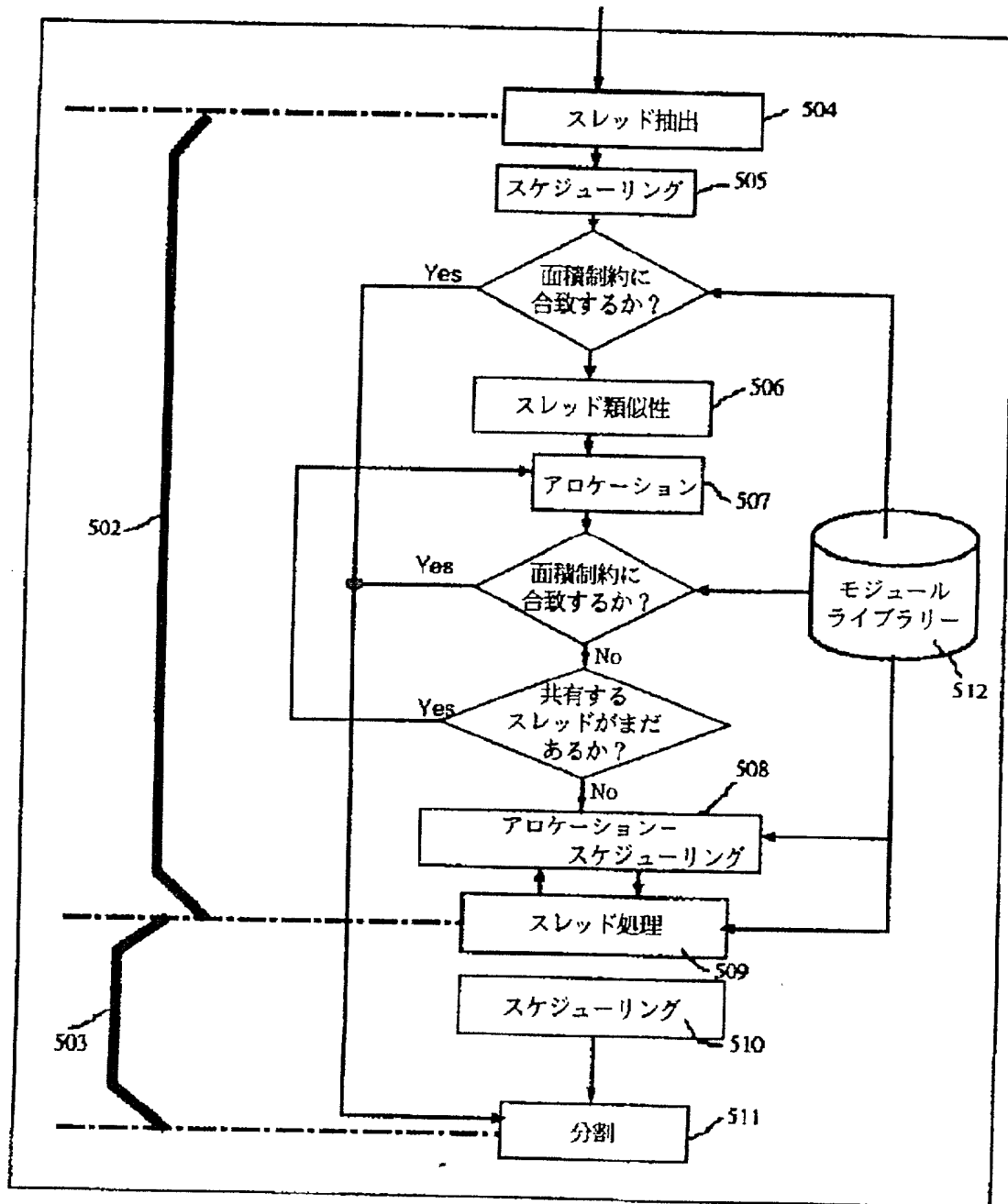


図 5



2D Motion Estimation
 for (i=0; i<Nx; i++)
 for (j=0; j<Ny; j++) {
 Diff[i][j] += img[i][j] - ref[i][j];
 }

601

2D Finite input Impulse Filer
 for (i=0; i<Nx; i++)
 for (j=0; j<Ny; j++) {
 Out[i][j] += img[i][j] x coeff[i][j];
 }

602

1D autocorrelation Filer
 for (i=0; i<Nx; i++) {
 Result[i] += in[i] - ref[i];
 }

603

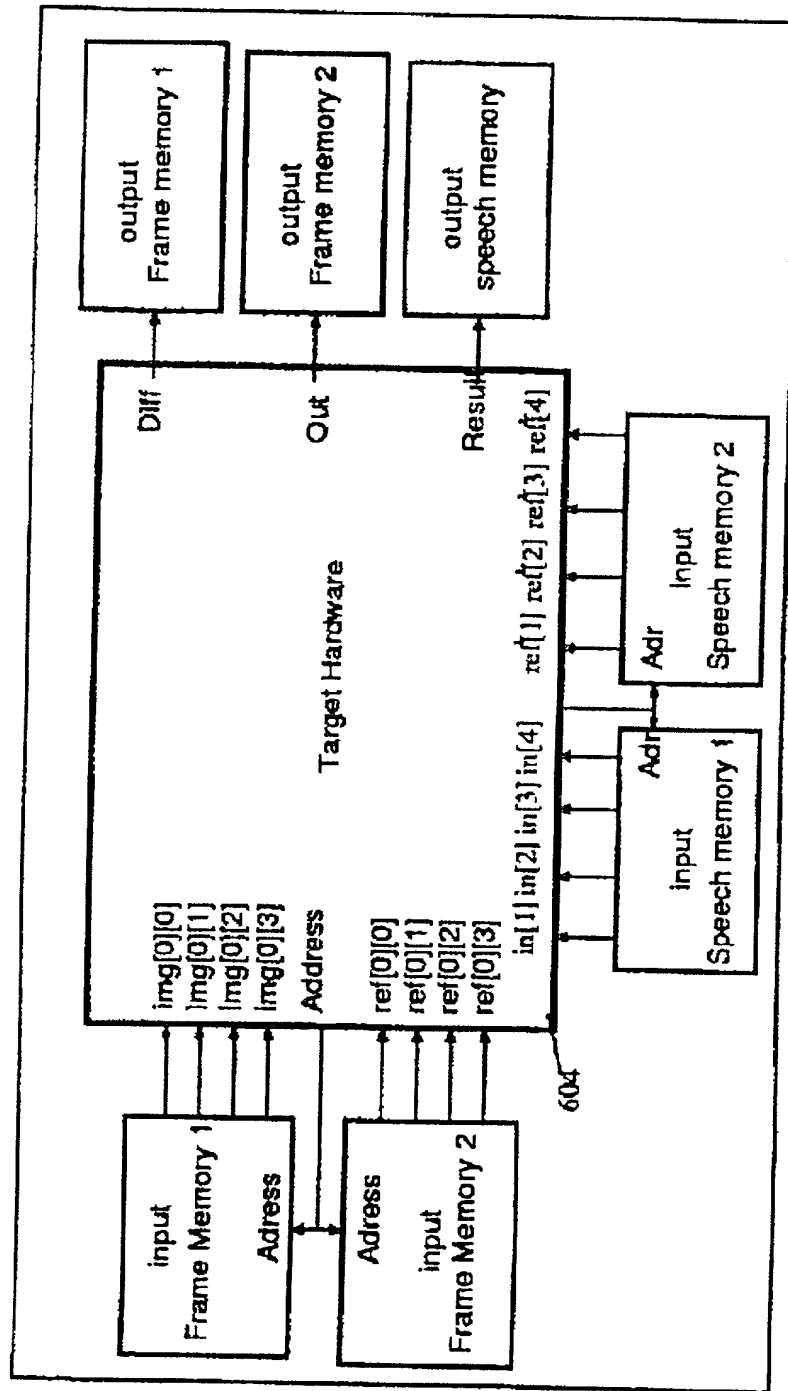
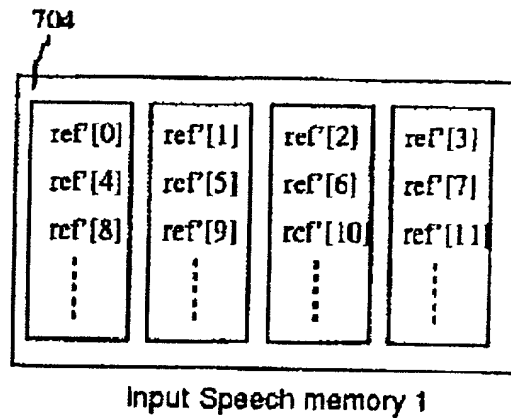
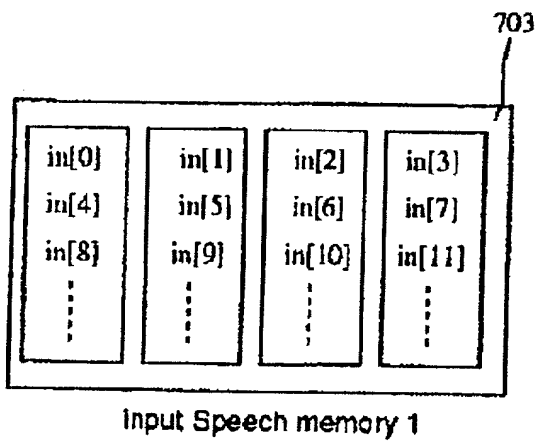
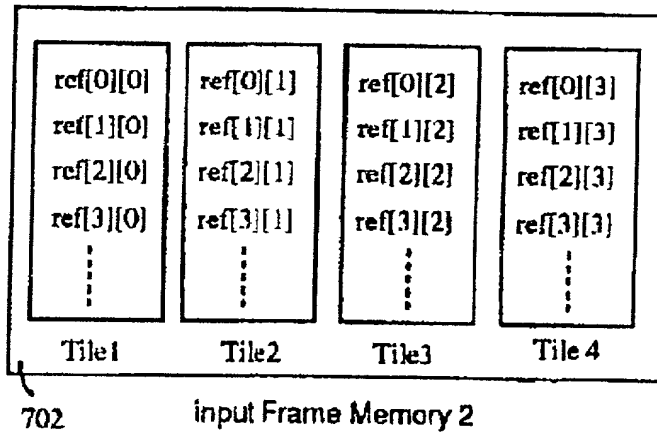
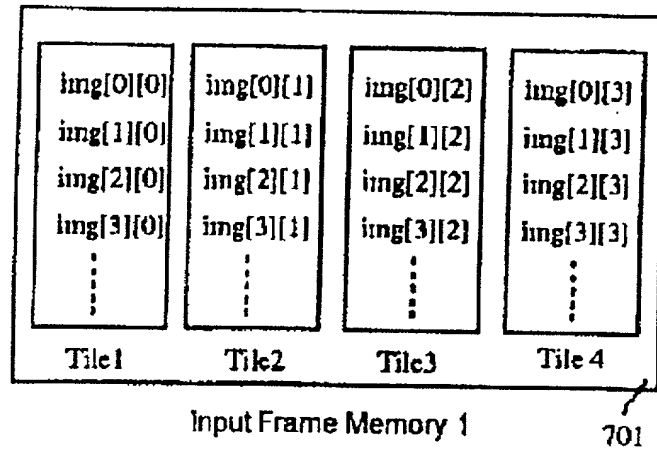
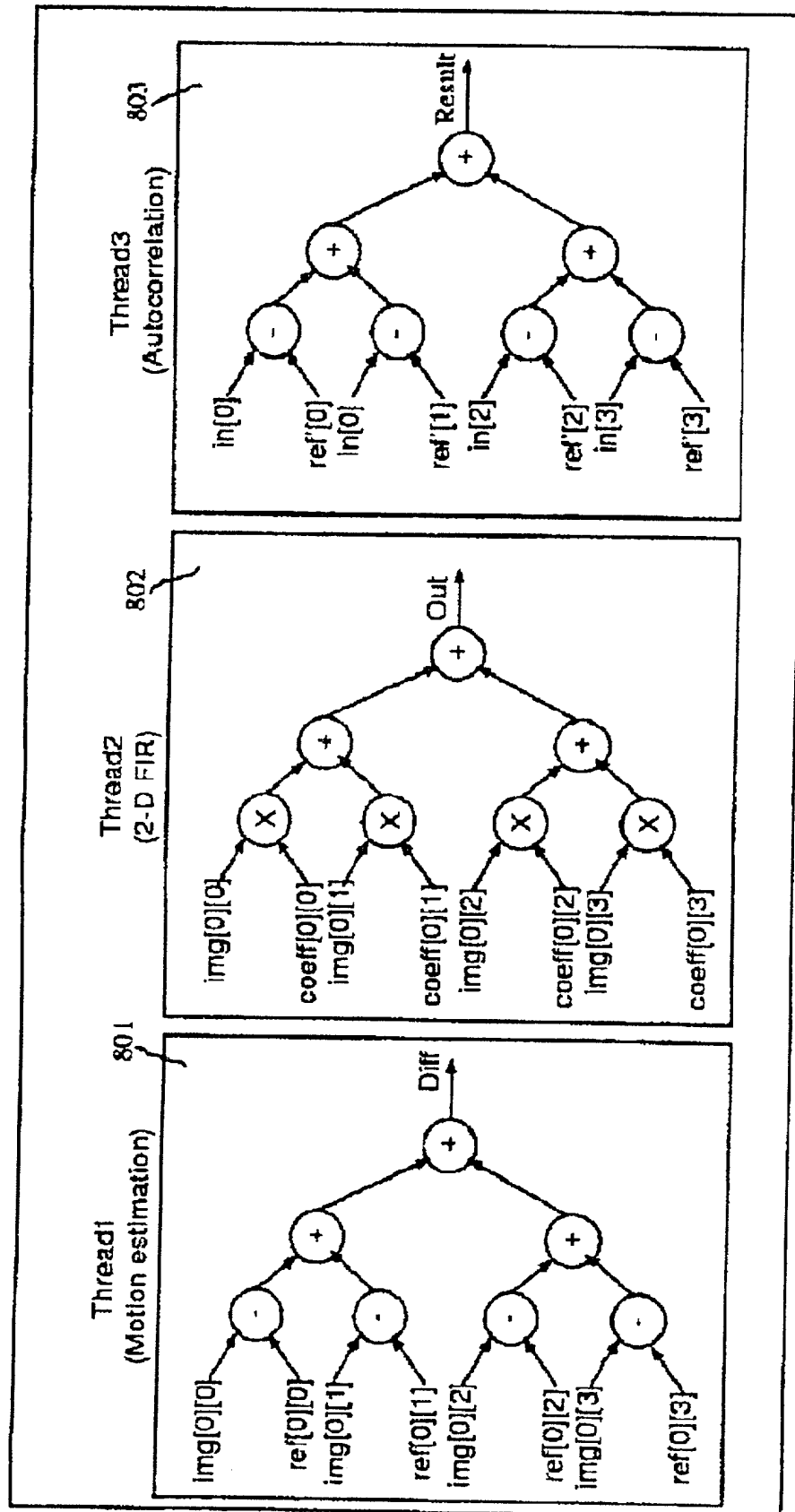


図 7



8



9

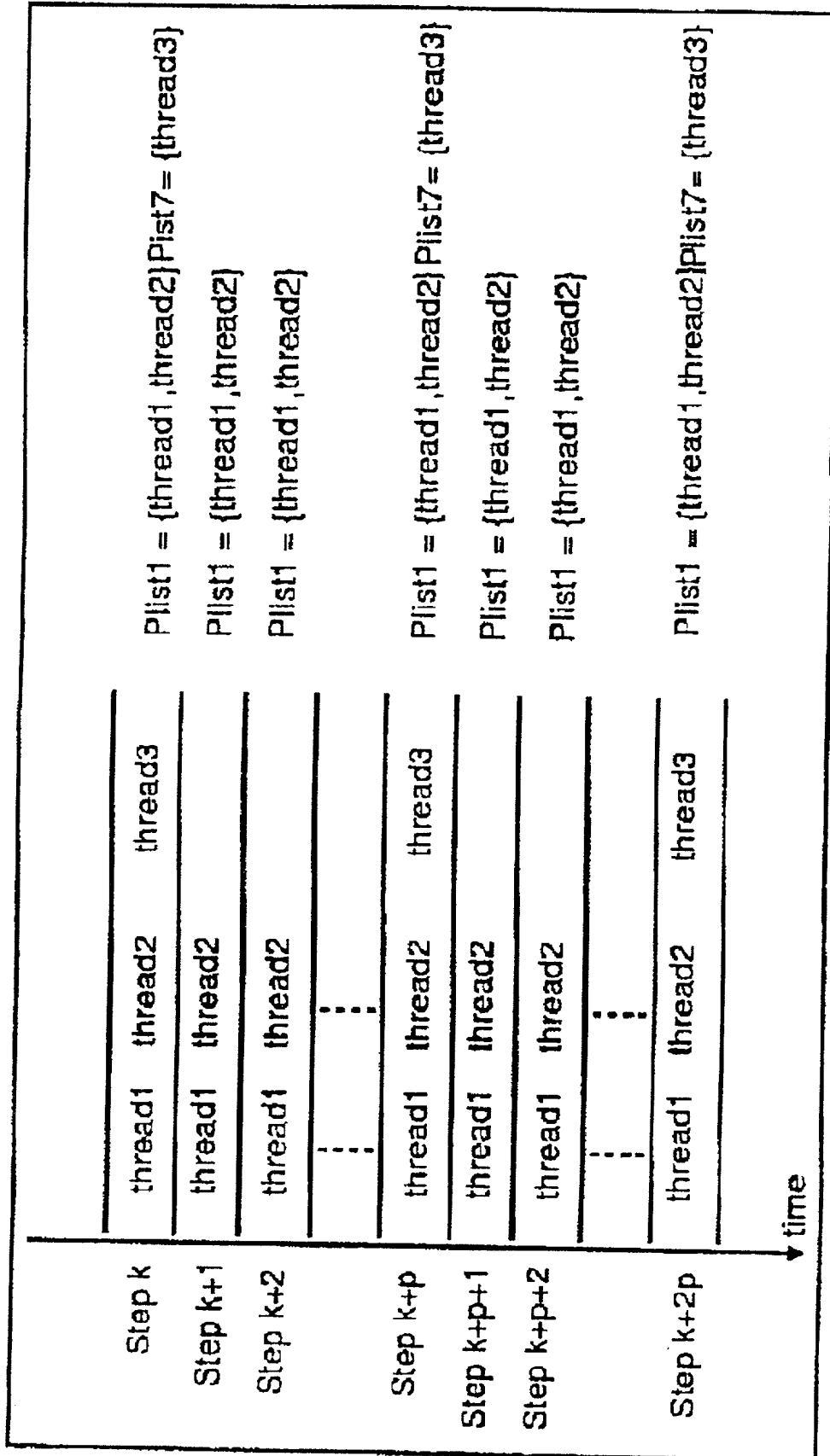
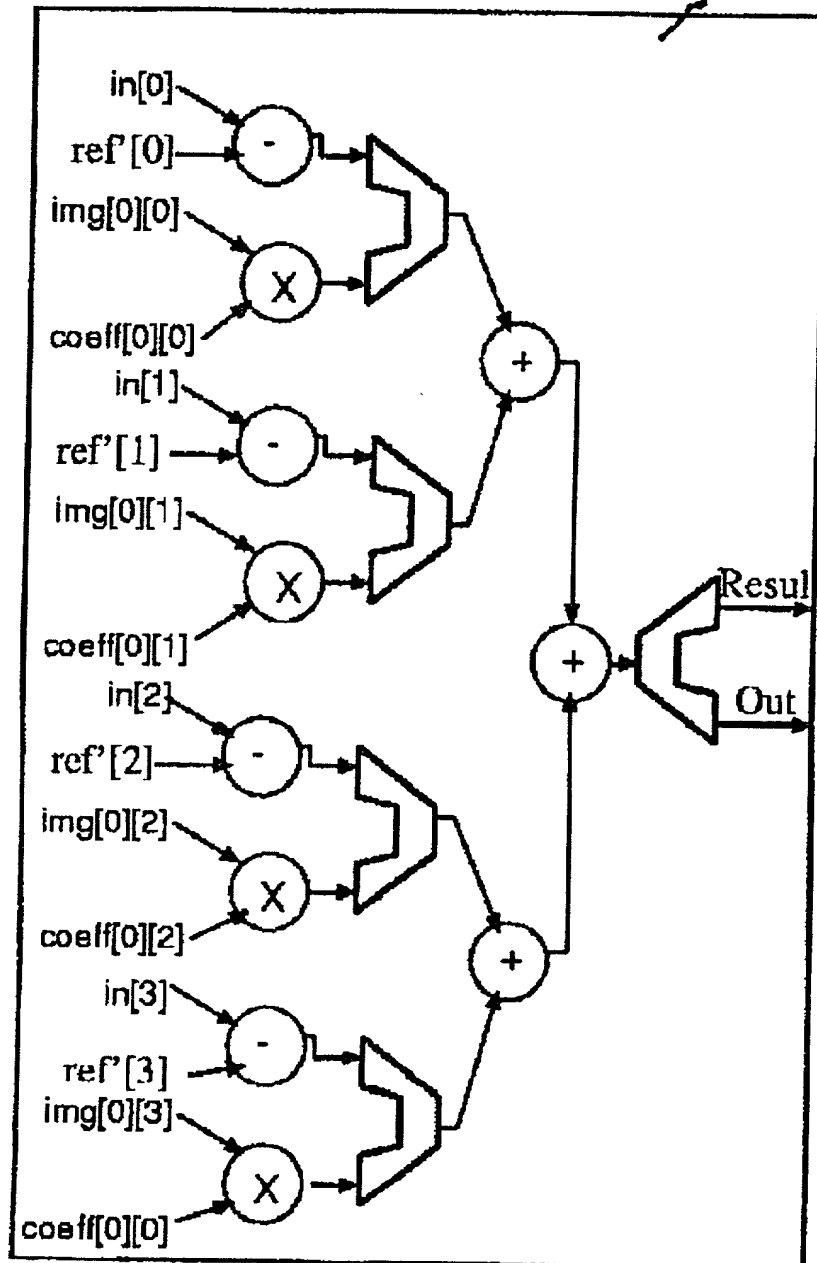


図 10

Thread2- Thread3

1003



Area13 = 410

[illegible]

図 11a

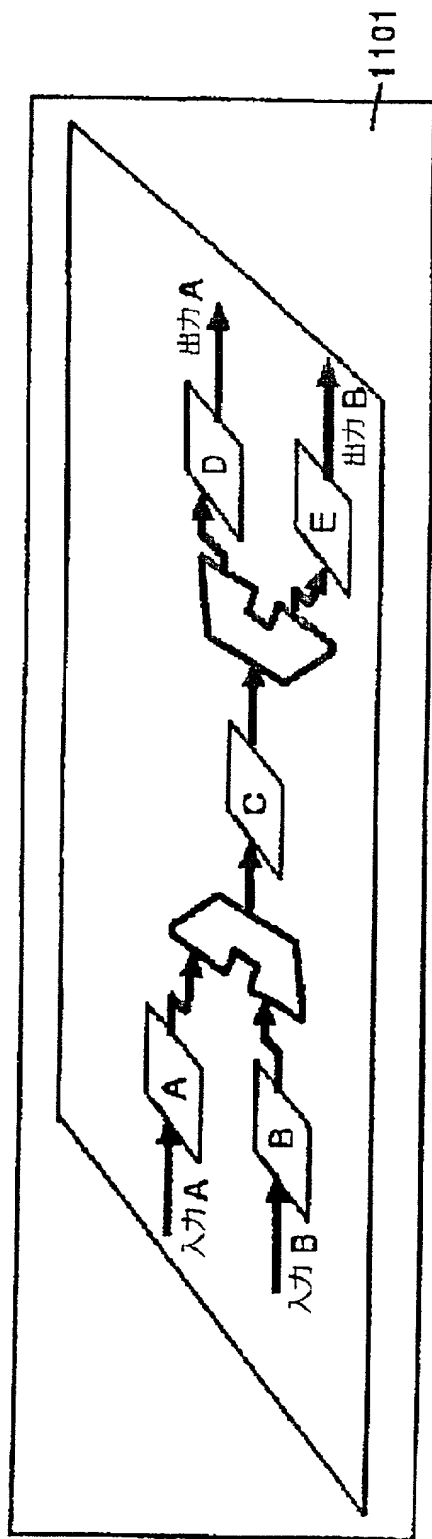
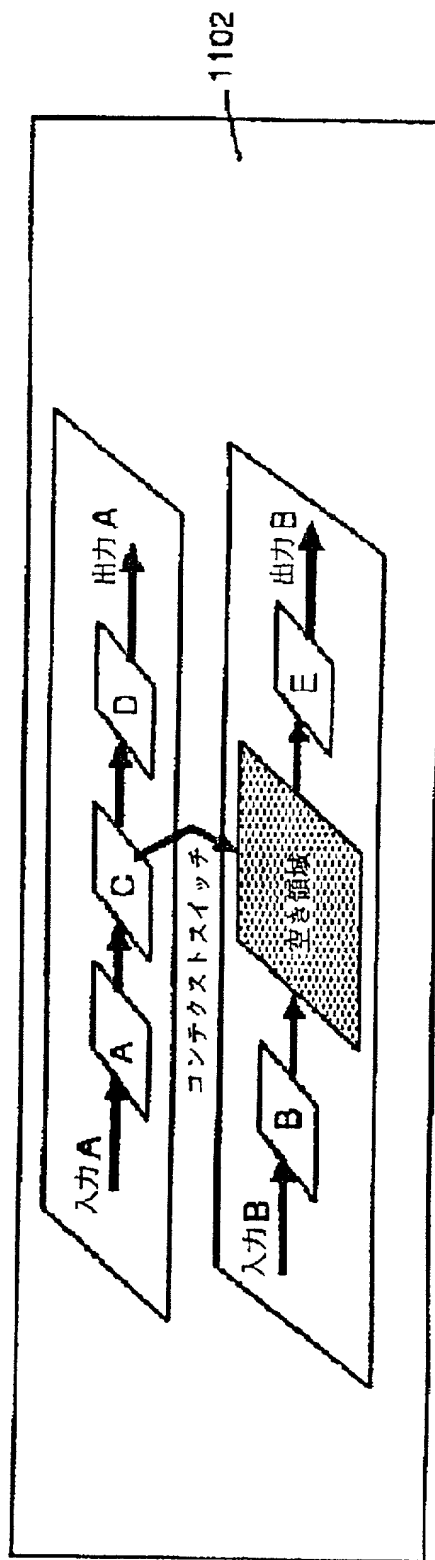


図 11b



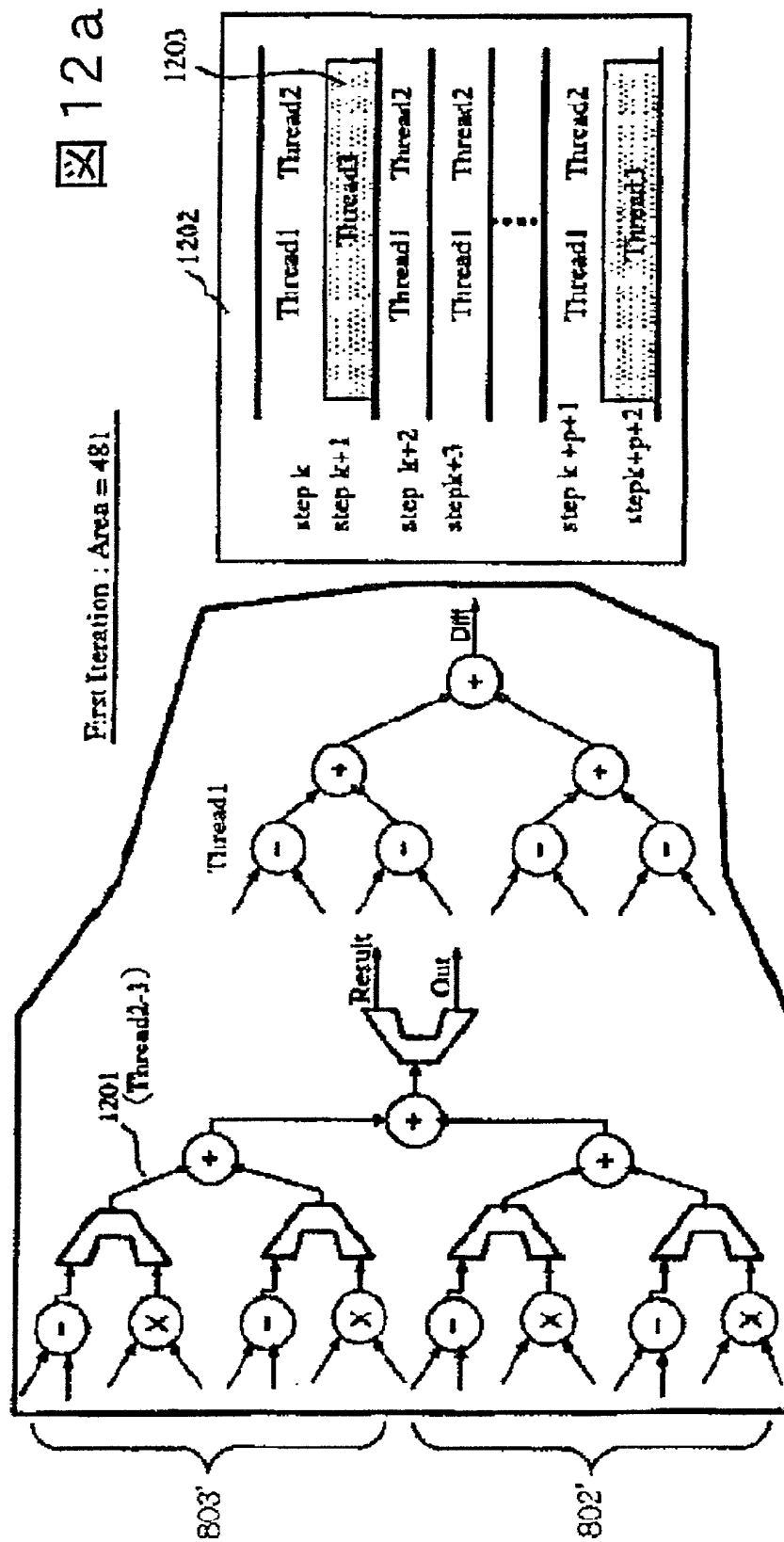
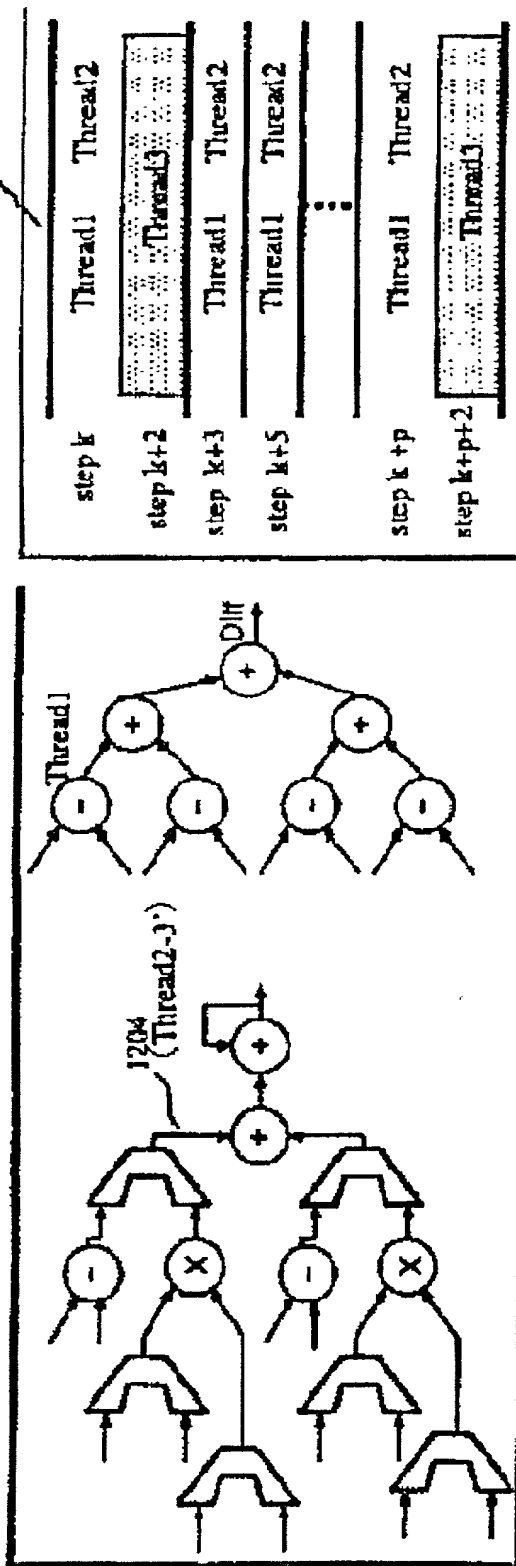
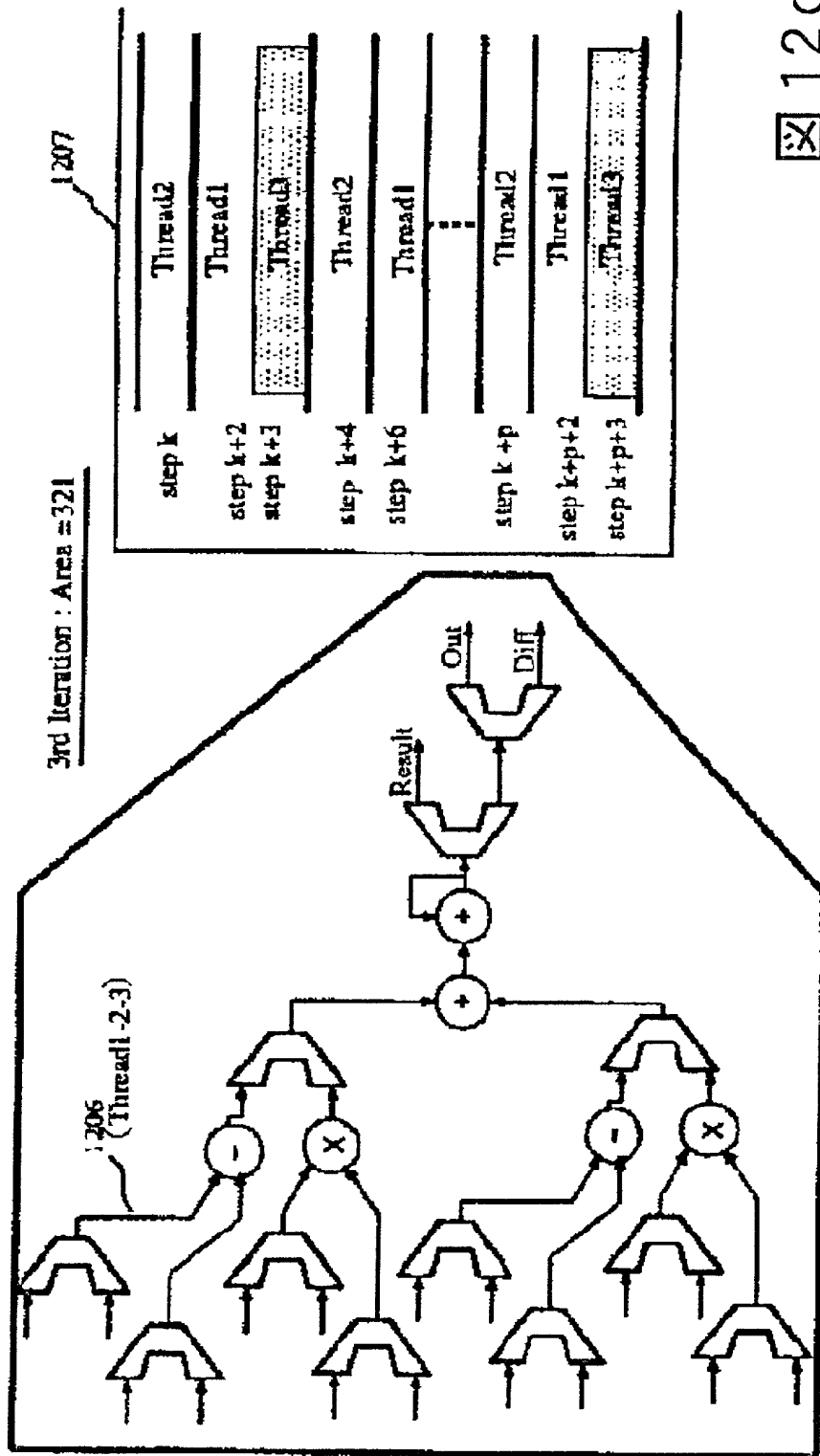


图 12b

2nd Iteration : Area = 368

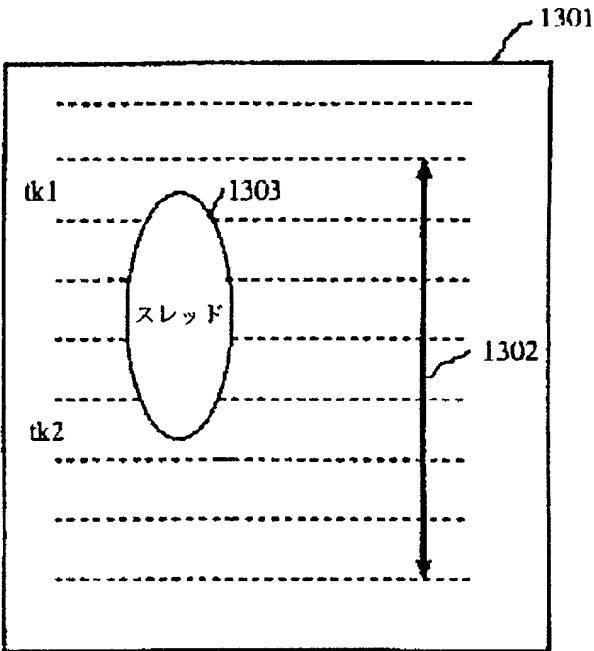
1205



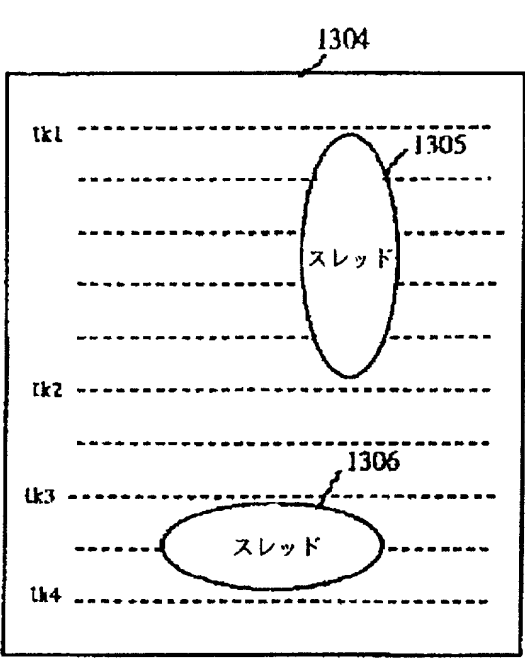


120

図 13 a

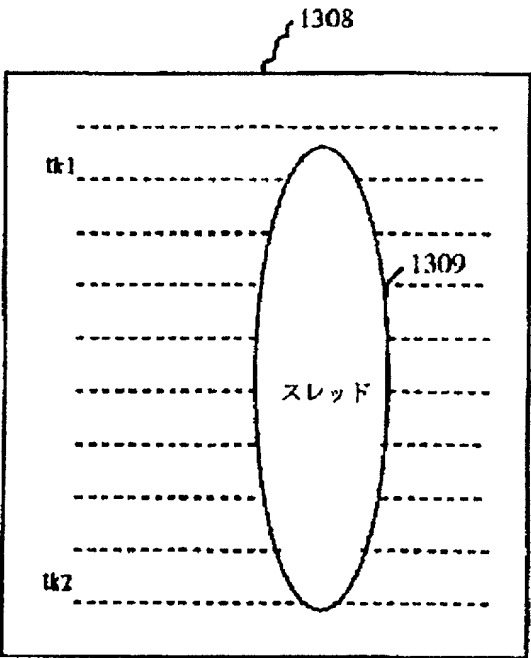


スレッド移動レンジ制約



スレッド共有制約

図 13 b



パイプライン制約

図 13 c

図 14

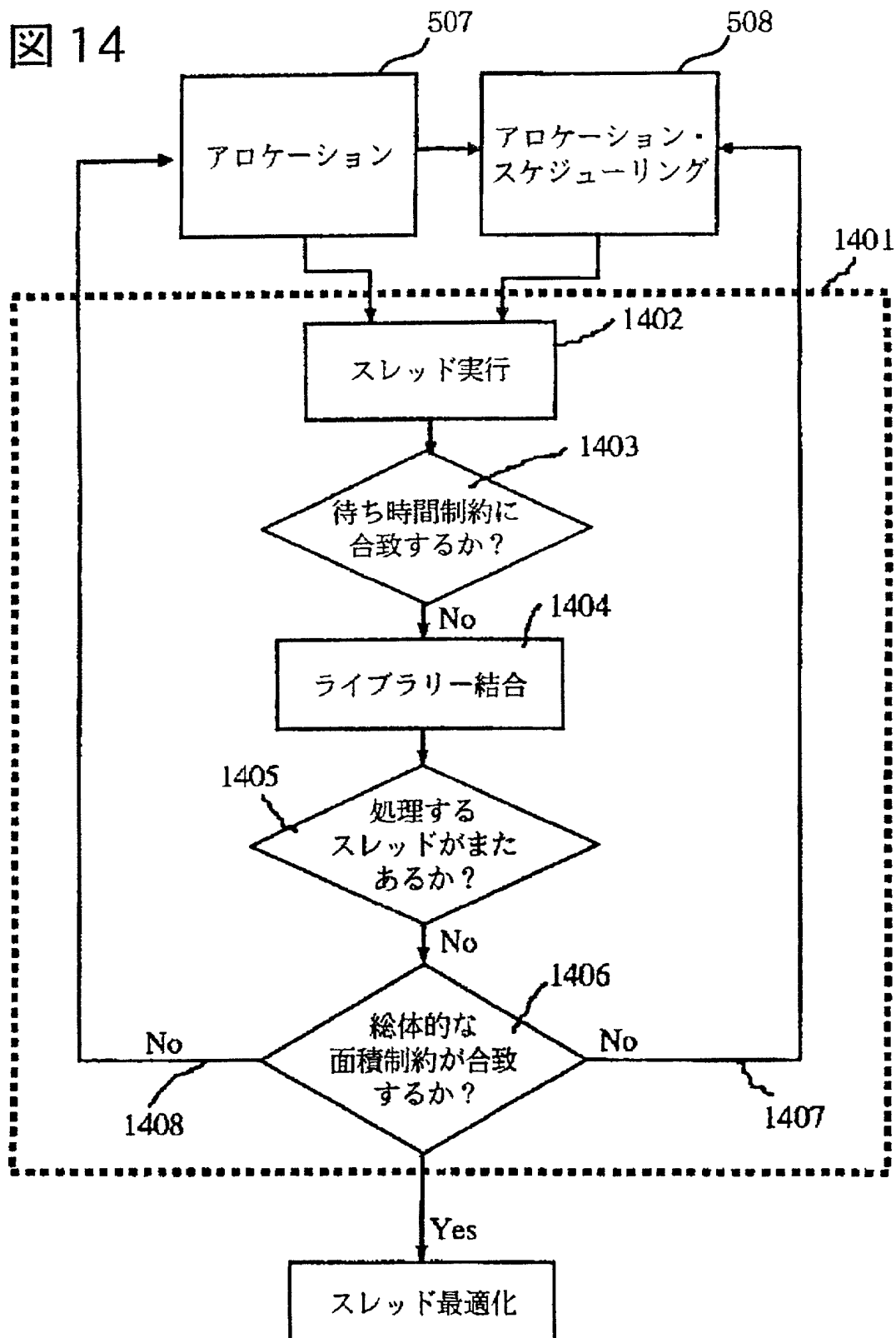
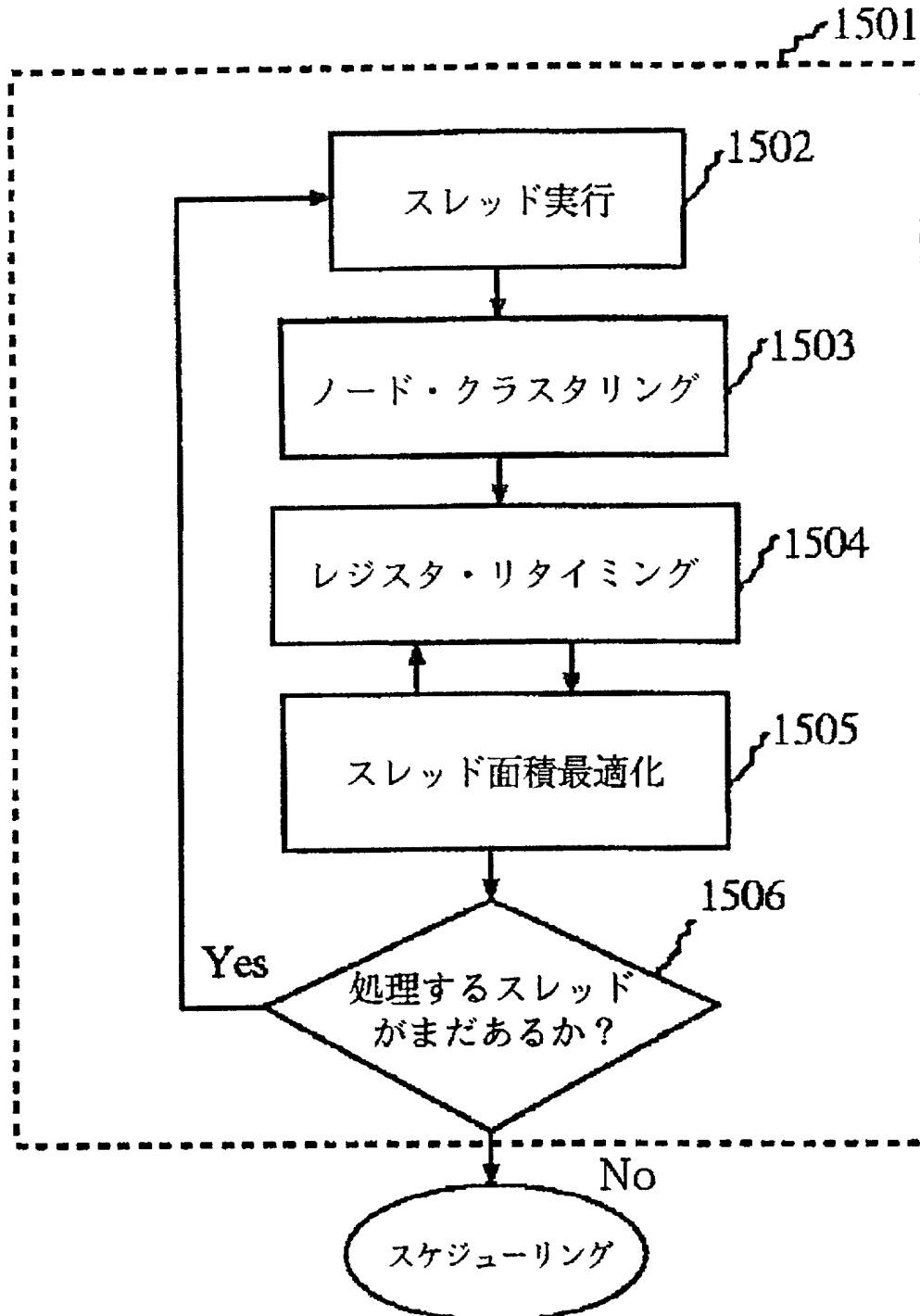
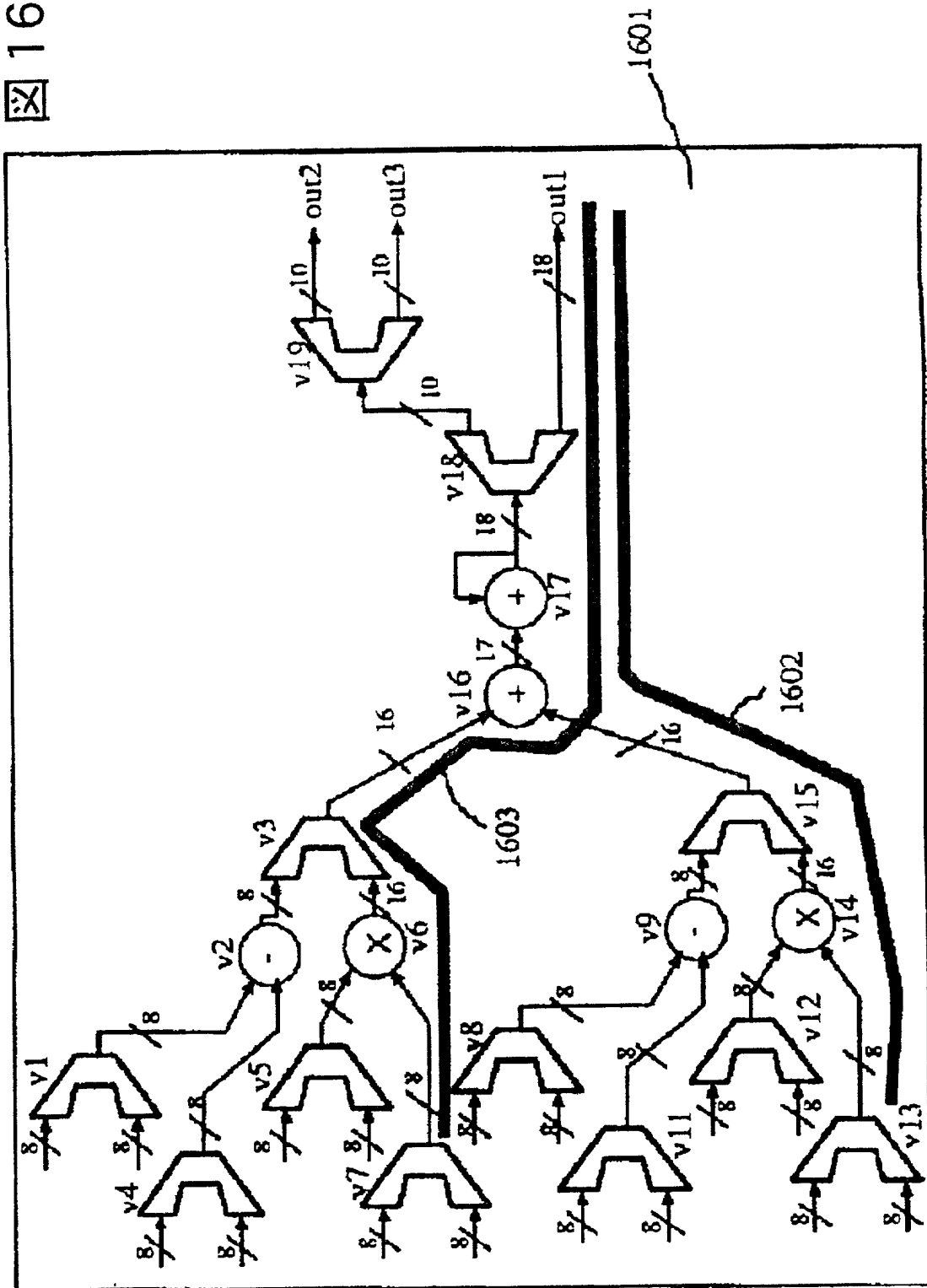


図 15



91 図



17

Closeness matrix

	v1	v2	v3	v4	v5	v6	v7	v8	v9	v10	v11	v12	v13	v14	v15	v16	v17	v18	v19
v1	-	8	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
v2	8	-	8	8	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
v3	-	8	-	-	-	16	-	-	-	-	-	-	-	-	-	16	-	-	-
v4	-	8	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
v5	-	-	-	-	-	8	-	-	-	-	-	-	-	-	-	-	-	-	-
v6	-	-	16	-	8	-	8	-	-	-	-	-	-	-	-	-	-	-	-
v7	-	-	-	-	-	8	-	-	-	-	-	-	-	-	-	-	-	-	-
v8	-	-	-	-	-	-	-	-	8	-	-	-	-	-	-	-	-	-	-
v9	-	-	-	-	-	-	8	-	-	8	-	-	-	-	8	-	-	-	-
v10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
v11	-	-	-	-	-	-	-	8	-	-	-	-	-	-	-	-	-	-	-
v12	-	-	-	-	-	-	-	-	-	-	-	-	-	8	-	-	-	-	-
v13	-	-	-	-	-	-	-	-	-	-	-	-	-	8	-	-	-	-	-
v14	-	-	-	-	-	-	-	-	-	-	-	8	8	-	8	-	-	-	-
v15	-	-	-	-	-	-	-	-	8	-	-	-	-	8	-	16	-	-	-
v16	-	-	16	-	-	-	-	-	-	-	-	-	-	-	16	-	17	18	-
v17	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
v18	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10
v19	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	-

1604

8
 1
 1

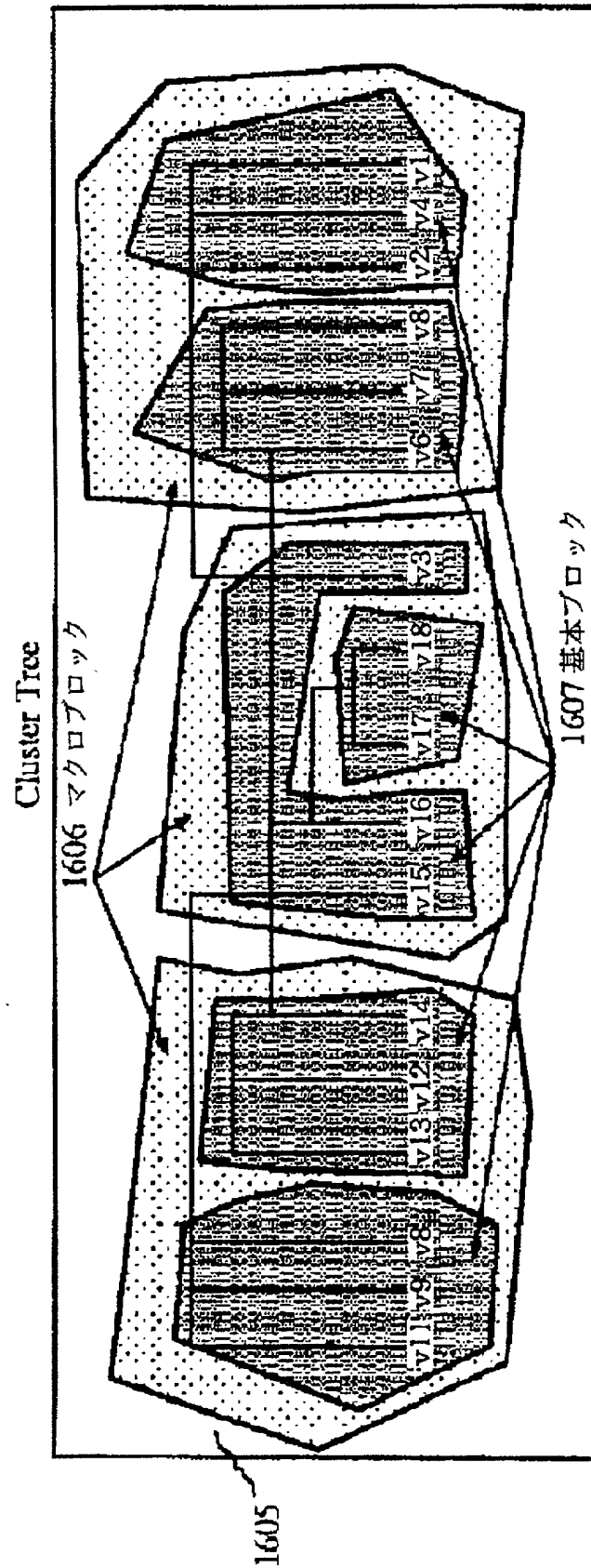


図 19

